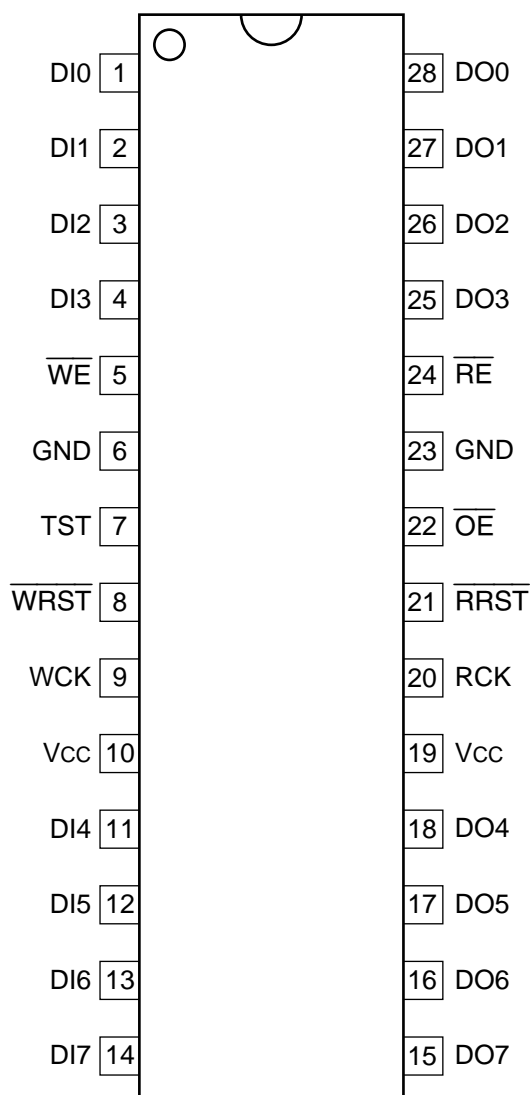


## 3M (393216 × 8)-BIT FIFO MEMORY

—TOP VIEW—

**INPUTS**

DI0 - DI7 : DATA  
 $\overline{OE}$  : OUTPUT ENABLE  
 $\overline{RCK}$  : READ CLOCK  
 $\overline{RE}$  : READ ENABLE  
 $\overline{RRST}$  : READ RESET  
TST : TEST  
WCK : WRITE CLOCK  
 $\overline{WE}$  : WRITE ENABLE  
 $\overline{WRST}$  : WRITE RESET

**OUTPUTS**

DO0 - DO7 : DATA

| PIN NO. | I/O | SIGNAL          | PIN NO. | I/O | SIGNAL            | PIN NO. | I/O | SIGNAL            | PIN NO. | I/O | SIGNAL          |
|---------|-----|-----------------|---------|-----|-------------------|---------|-----|-------------------|---------|-----|-----------------|
| 1       | I   | DI0             | 8       | I   | $\overline{WRST}$ | 15      | O   | DO7               | 22      | I   | $\overline{OE}$ |
| 2       | I   | DI1             | 9       | I   | WCK               | 16      | O   | DO6               | 23      | —   | GND             |
| 3       | I   | DI2             | 10      | —   | Vcc               | 17      | O   | DO5               | 24      | I   | $\overline{RE}$ |
| 4       | I   | DI3             | 11      | I   | DI4               | 18      | O   | DO4               | 25      | O   | DO3             |
| 5       | I   | $\overline{WE}$ | 12      | I   | DI5               | 19      | —   | Vcc               | 26      | O   | DO2             |
| 6       | —   | GND             | 13      | I   | DI6               | 20      | I   | $\overline{RCK}$  | 27      | O   | DO1             |
| 7       | I   | TST             | 14      | I   | DI7               | 21      | I   | $\overline{RRST}$ | 28      | O   | DO0             |